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	09/649,437 08/23/2000		Bart Reynolds		lds	003242.P017	6896	
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inventor(s):

Bart Reynolds et al.

Assignee: XILINX, INC.

Serial No.: 09/649,437

conf.no. 6896

Filed:

August 23, 2000

Method and Apparatus for Specifying Addressability and Bus Connections in a Logic

Design

Docket No.:

X-1770 US

Enclosed: Return Receipt Postcard

Fee Transmittal - Part B

Date: February 14, 2005

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